



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): Joo-Hoon Lee et al.

SERIAL NO.: 10/600,686

EXAMINER: Unassigned

DATE FILED: June 20, 2003

ART UNIT: Unassigned

**FOR: METHOD FOR FABRICATING PLANAR LIGHT WAVEGUIDE CIRCUITS  
WITH VERTICAL TAPER STRUCTURE**

**CERTIFICATE OF MAILING UNDER 37 CFR §1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the MAIL STOP DD, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA. 22313 on 2/24/04

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(Name of depositor)

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT**

In accordance with Applicant's and Applicant's representatives' Duty of Disclosure under 37 CFR § 1.56, and pursuant to 37 CFR §1.97 and MPEP 717.05(b), Applicant(s) submit herewith documentary information for consideration by the Examiner. Information herein cited is only set forth in fulfillment of Applicant's duty of candor in disclosing all information brought to his attention, and is not an admission that it can be used adversely. The publications forwarded herewith are listed on the enclosed Form PTO-1449. Applicant(s) request that the Examiner, upon reviewing the enclosed materials, initial the enclosed form and return a copy thereof in accordance with the instructions on the form.

Enclosed please find copies of 1 U.S. Patent, 2 Foreign Patents and 2 Foreign Publications listed on the attached Form PTO-1449. No fee is believed due for the filing of this statement inasmuch as it is being filed before the mailing of the first official action on the merits. However, should the Patent and Trademark Office determine otherwise, authorization is hereby given to charge Deposit Account No. 502-470 for this filing.

Respectfully submitted,

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Sheet 1 of 1

Form PTO-1449 IRSY. 7.801 U.S. Department of Commerce Patent and Trademark Office	ATTORNEY DOCKET NO.	5000-1-307
	SERIAL NO.	10/600,686
LIST OF DOCUMENTARY INFORMATION CITED BY APPLICANT (Use several sheets if necessary)	APPLICANT	Joo-Hoon Lee et al.
	FILING DATE	June 20, 2003
	GROUP	Unassigned

U.S. PATENT DOCUMENTS

EXAMINE R INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA	5,323,476	6/21/94	Mueller et al.	385	43	07/16/93

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
	AB	WO02-063347A2	8/15/02	PCT	G02B		X
	AC	JP10090537	4/10/98	Japan	G02B6	122	X

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AD	"Very Low Insertion Loss Arrayed-Waveguide Grating with Vertically Tapered Waveguides;" A. Sugita et al., XP000968634; September 2000; pgs. 1180-1182.
	AE	"A Review on Fabrication Technologies for the Monolithic Integration of Tapers with III-V Semiconductor Devices;" I. Moerman et al.; XP002146284; December 1997; pgs. 1308-1320.

EXAMINER:

DATE CONSIDERED:

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.